# CMPE 240 Experiment 5 Preliminary Works

1. State Register Inputs: S1, S0
2. State Register Outputs: N1, N0
3. Combinational Block Inputs: A, B
4. Combinational Block Outputs: Y1, Y0
5. Is this a Moore or Mealy Machine? (No explanation, only short answer): Mealy
6. Draw the finite state machine.

